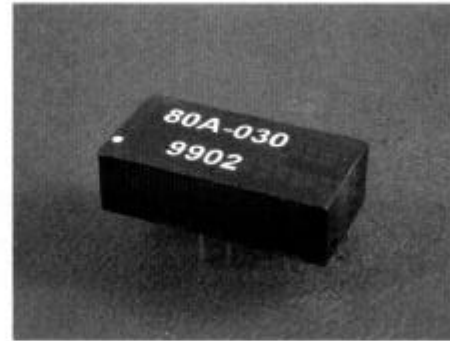


FEATURES

- 16-PIN PACKAGE.
- TTL SCHOTTKY INTERFACED.
- INHERENT DELAY: 7 ± 1 ns
- TOTAL DELAY TOLERANCE: $\pm 5\%$ OR ± 1 ns

ELECTRICAL CHARACTERISTICS

I _{IH} Logic "1" Input Current	:50uA max
I _{IL} Logic "0" Input Current	:-2mA max
V _{OH} Logic "1" Output Voltage	:2.4V min
V _{OL} Logic "0" Output Voltage	:0.4V max
V _{IH} Logic "1" Input Voltage	:2.0V min
V _{IL} Logic "0" Input Voltage	:0.8V max
T _A Operating Temperature	:0°C to 70°C
N _H Fanout "1" Output	:20 TTL Load
N _L Fanout "0" Output	:10 TTL Load
Supply Current	:85mA Typ.



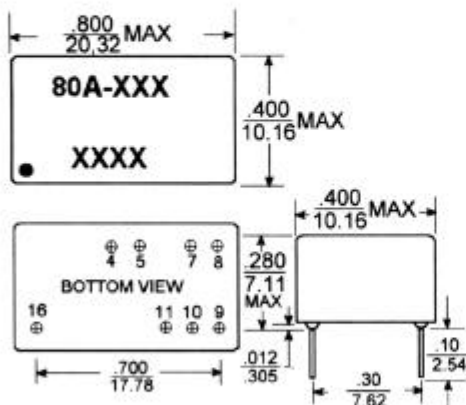
INPUT PULSE TEST CONDITION

Pulse Voltage	:3V
Pulse Width	:3Td
Duty Cycle	:1000ns
Pulse Rise Time	:2nS(0.75 to 2.4V)
Time Delay Measured	@1.5V level
Supply Voltage V _{cc}	:5.0 \pm 0.25Vdc

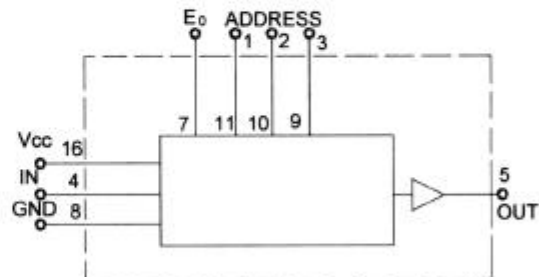
ELECTRICAL SPECIFICATIONS

Program Part numbers	0	1	2	3	4	5	6	7	Tol. Of Prog. Delay	Delay Time Per. Step	Delay Time Step Tol.	Maximum Delay Nominal Ref. IN
80A-014	7 \pm 1 ns	1	2	3	4	5	6	7	± 0.5 ns	1 ns	± 0.3 ns	14 ns
80A-021	7 \pm 1 ns	2	4	6	8	10	12	14	± 0.6 ns	2 ns	± 0.4 ns	21 ns
80A-028	7 \pm 1 ns	3	6	9	12	15	18	21	± 0.7 ns	3 ns	± 0.5 ns	28 ns
80A-035	7 \pm 1 ns	4	8	12	16	20	24	28	± 0.9 ns	4 ns	± 0.5 ns	35 ns
80A-042	7 \pm 1 ns	5	10	15	20	25	30	35	± 1 ns	5 ns	± 0.5 ns	42 ns
80A-049	7 \pm 1 ns	6	12	18	24	30	36	42	± 1.2 ns	6 ns	± 0.6 ns	49 ns
80A-056	7 \pm 1 ns	7	14	21	28	35	42	49	± 1.4 ns	7 ns	± 0.7 ns	56 ns
80A-063	7 \pm 1 ns	8	16	24	32	40	48	56	± 1.6 ns	8 ns	± 0.8 ns	63 ns
80A-070	7 \pm 1 ns	9	18	27	36	45	54	63	± 1.8 ns	9 ns	± 0.9 ns	70 ns
80A-077	7 \pm 1 ns	10	20	30	40	50	60	70	± 2.0 ns	10 ns	± 1 ns	77 ns
80A-091	7 \pm 1 ns	12	24	36	48	60	72	84	± 2.0 ns	12 ns	± 1.2 ns	91 ns

MARKINGS AND DIMENSIONS



PIN CONNECTIONS



Dimensions: inches/mm

Unless otherwise specified, all tolerances are $\pm .010 / \pm 0.25$

ALLEN AVIONICS, INC.